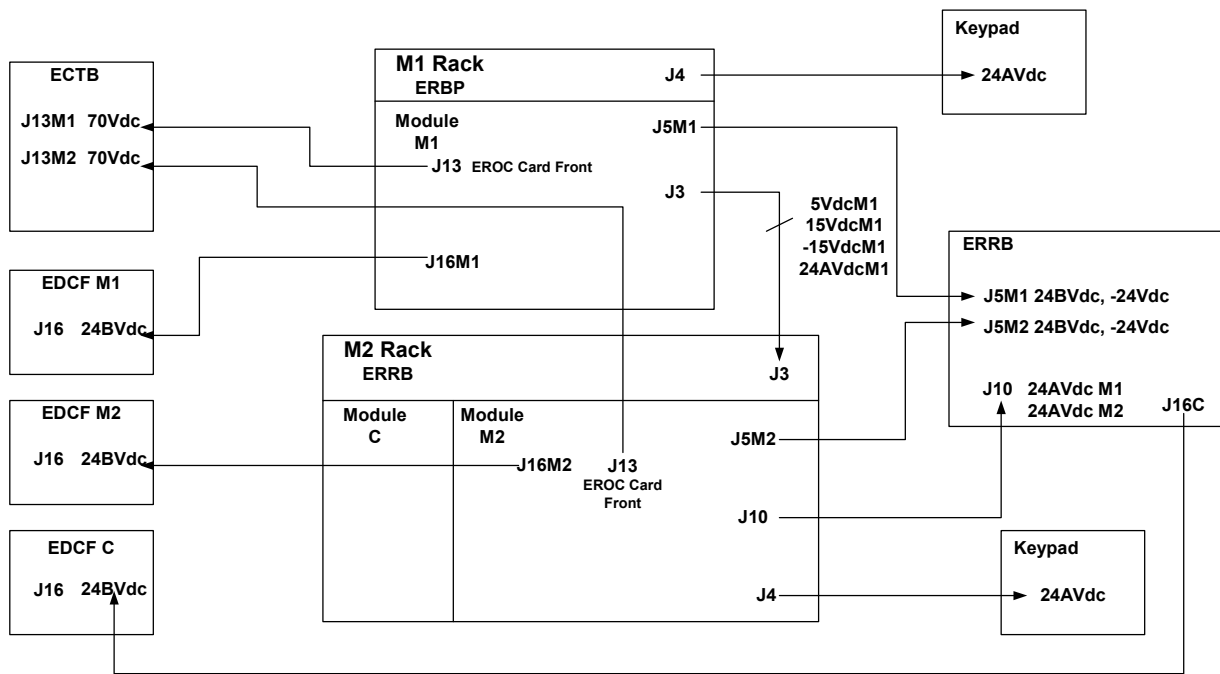




Redundant System Power Block Diagram

Exciter Regulator I/O (ERIO)

The ERIO board provides the I/O interface for customer and system I/O in the EX2100 Regulator Control systems (simplex and redundant). For **simplex** applications, it is mounted in ERBP and interfaces customer I/O signals for the EPCT and ECTBG2 terminal boards. It also interfaces system I/O signals for the ERDD and the EROC for simplex applications.

For **redundant** applications, it is mounted in the ERBP (M1) and also in the ERRB (M2/C) and interfaces customer I/O signals for the EPCT and ECTBG1 terminal boards. It also interfaces system I/O signals for the ERDD, EROC, and the IS200ERRR Exciter Regulator Redundant Relay board (ERRR) for redundant applications.

The ERIO, EPCT and ECTB boards are interconnected through two 25-pin connectors to ERBP (M1 in redundant) and also to ERRB (M2/C) in redundant applications. One ERIO each is provided in the M1, M2, and C control sections.

An onboard field programmable gate array (FPGA) is used to control data acquisition and storage into dual port memory (DPM). The DPM is accessible by the DSPX board from the backplane.

The ERIO, EROC and ERDD boards are connected through the ERBP (simplex) and ERRB (redundant) backplanes. The ERIO and ERRR are interconnected through one 25-pin connector in the backplane (ERBP, ERRB).

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